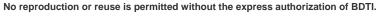
Speed Scores for Floating-Point Packaged Processors (Higher is Better) BDTImark2000™ and BDTIsimMark2000™ (Single-Core Scores)

Updated May 2013

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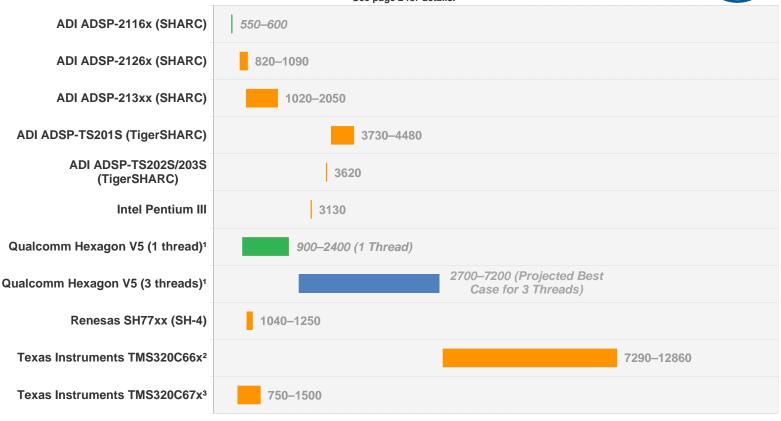


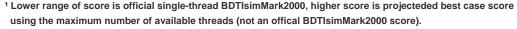


■ Projected Best Case Multi-Thread Result

■BDTImark2000[™]

■ BDTIsimMark2000[™]





² Score for one core

BDTIsimMark2000™ scores may be based on projected clock speeds. For information, see www.BDTI.com/Services/Benchmarks

³ Score does not apply to TMS320C67x+ parts (e.g., the TMS320C672x) All processors benchmarked with 32-bit floating-point data.

Speed Scores for Floating-Point Packaged Processors (Higher is Better) BDTImark2000™ and BDTIsimMark2000™ (Single-Core Scores)

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Processor Family	Clock Rate (min-max)	BDTImark2000™, <i>BDTIsimMark2000</i> ™ (min-max)
ADI ADSP-2116x (SHARC)	100-110 MHz	550–600
ADI ADSP-2126x (SHARC)	150-200 MHz	820–1090
ADI ADSP-213xx (SHARC)	200–400 MHz	1020–2050
ADI ADSP-TS201S (TigerSHARC)	500–600 MHz	3730–4480
ADI ADSP-TS202S/203S (TigerSHARC)	500 MHz	3620
Intel Pentium III	1400 MHz	3130
Qualcomm Hexagon V5 (1 thread) ¹	100-267 MHz (per thread)	900–2400 (1 Thread)
Qualcomm Hexagon V5 (3 threads) ¹	100-267 MHz (per thread)	2700–7200 (Projected Best Case for 3 Threads)
Renesas SH77xx (SH-4)	333-400 MHz	1040–1250
Texas Instruments TMS320C66x ²	850-1500 MHz	7290–12860
Texas Instruments TMS320C67x3	150-300 MHz	750–1500

¹ Lower range of score is official single-thread BDTIsimMark2000, higher score is projecteded best case score using the maximum number of available threads (not an offical BDTIsimMark2000 score).

All processors benchmarked with 32-bit floating-point data.

BDTImark2000[™], BDTIsimMark2000[™]: The BDTImark2000[™] and BDTIsimMark2000[™] provide a summary measure of signal processing speed. BDTIsimMark2000[™] scores may be based on projected clock speeds. For more info and scores see www.BDTI.com/Services/Benchmarks.

² Score for one core

³ Score does not apply to TMS320C67x+ parts (e.g., the TMS320C672x)